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<b>PRE-APPEAL BRIEF REQUEST FOR REVIEW</b>		Docket Number (Optional) <b>ITL.1703US (P17498)</b>
<p>I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR</p> <p>on <u>January 25, 2008</u></p> <p>Signature </p> <p>Typed or printed name <u>Nancy Meshkoff</u></p>		<p>Application Number <b>10/750,075</b></p> <p>Filed <b>December 31, 2003</b></p> <p>First Named Inventor <b>Louis Lippincott</b></p> <p>Art Unit <b>2621</b></p> <p>Examiner <b>Chikaodili E. Anyikire</b></p>
<p>Applicant requests review of the final rejection in the above-identified application. No amendments are being filed with this request.</p> <p>This request is being filed with a notice of appeal.</p> <p>The review is requested for the reason(s) stated on the attached sheet(s).</p> <p>Note: No more than five (5) pages may be provided.</p>		
<p>I am the</p> <p><input type="checkbox"/> applicant/inventor.</p> <p><input type="checkbox"/> assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)</p> <p><input checked="" type="checkbox"/> attorney or agent of record. Registration number <u>28,994</u></p> <p><input type="checkbox"/> attorney or agent acting under 37 CFR 1.34. Registration number if acting under 37 CFR 1.34 _____</p>		 <p>Signature</p> <p><u>Timothy N. Trop</u></p> <p>Typed or printed name</p> <p><u>(713) 468-8880</u></p> <p>Telephone number</p> <p><u>January 25, 2008</u></p> <p>Date</p>
<p>NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below*.</p>		
<p><input type="checkbox"/> *Total of _____ forms are submitted.</p>		

This collection of information is required by 35 U.S.C. 132. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11, 1.14 and 41.6. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Louis Lippincott et al.

§ Art Unit: 2621

Serial No.: 10/750,075

§ Examiner: Chikaodili E. Anyikire

Filed: December 31, 2003

§ Docket: ITL.1703US  
P17498

For: Motion Estimation Sum of all  
Differences (SAD) Array Having  
Reduced Semiconductor Die  
Area Consumption

§ Assignee: Intel Corporation

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**STATEMENT IN SUPPORT OF PRE-APPEAL BRIEF REQUEST FOR REVIEW**

Sir:

Reconsideration of the rejection of claim 13 is respectfully requested, because it is clear that the amended claim language (underlined) has never been addressed:

13. An apparatus, comprising:
- logic circuitry to take an absolute difference between:
    - less than all of the bits of an uncompressed video data value from a reference macro block;
    - less than all of the bits of an uncompressed video data value from a macro block worth of data within a search window;
  - a circuit to calculate a number of most significant bits to mask;
  - a register to store said reference macro block, said register coupled to said logic circuitry; and
  - a random access memory to store said search window, said random access memory coupled to said logic circuitry.

Date of Deposit: January 25, 2008

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Nancy Meshkoff

The argument on page 2, paragraph 2 of the final rejection that Lin teaches "the circuit and the ability to take the most significant bits" is not commensurate with the claim language.

Similarly, the assertion in the same paragraph of the final rejection that the claim was amended to introduce "a circuit to mask the most significant bits" misstates the claim language.

Lin cannot teach a circuit to calculate a number of most significant bits to mask. As pointed out in the final office action, Lin simply masks the six most significant of eight bits. He has nothing to determine and, most certainly, does not calculate anything. Therefore, he cannot possibly show a circuit to calculate a number of most significant bits to mask and nothing cited in the office action points to such a thing.

As a result, there is a need to reconsider the rejection of claim 13.

Respectfully submitted,

Date: January 25, 2008

  
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